



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Satoshi INOUE

Application No.: 10/814,169

Docket No.: 119330

Filed: April 1, 2004

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, INTEGRATED
CIRCUIT, ELECTRO-OPTICAL DEVICE, AND ELECTRONIC APPARATUS

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

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Date: August 3, 2004

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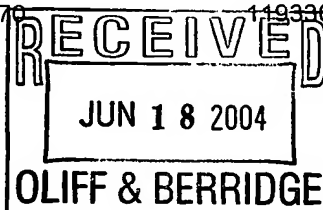


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APPL NO.	FILING OR 371 (c) DATE	PART UNIT & TRADEMARK OFFICE	FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/814,169	04/01/2004	2811	770	119336	11	17	1

25944
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CONFIRMATION NO. 7384

FILING RECEIPT



OC000000012966428

Date Mailed: 06/16/2004

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Assignment For Published Patent Application
 SEIKO EPSON CORPORATION, Tokyo, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications
 JAPAN 2003-107071 04/10/2003

If Required, Foreign Filing License Granted: 06/16/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

Title

Method of manufacturing semiconductor device, integrated circuit, electro-optical device, and electronic apparatus

Preliminary Class

257

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